

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Divisional Application of:)	
Takeshi FUKUNAGA)	Art Unit Group: 2823
Based on US Serial No. 09/907,911)	Examiner: William M. Brewster
Which Was Filed: July 19, 2001)	Confirmation No.: 1819
For: PROCESS FOR PRODUCTION OF SOI SUBSTRATE)	
AND PROCESS FOR PRODUCTION OF SEMICONDUCTOR)	Date: August 4, 2003
DEVICE		

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA. 22313-1450 Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98. All the references cited on the PTO-1449 From were filed in the parent application Serial No.: 09/907,911 filed July 19, 2001.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

Donald R. Studebaker

Registration No. 32,815

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DRS/JLC/sas

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PTO/SB/08A (08-00)

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	for form 1449A/PTO	<u> </u>	1	Complete if Known		
INEO	DMATIONI	NISCI	OSTIDE	Application Number	Not Yet Assigned	
INFORMATION DISCLOSURE				Filing Date	July 19, 2001	
SIAI	STATEMENT BY APPLICANT		First Named Inventor	Takeshi FUKUNAGA		
	(use as many sheets as necessary)			Group Art Unit	2823	
				Examiner Name	William M. Brewster	
Sheet	1	of	2	Attorney Docket Number	740756-2641	

				U.S. PATENT DOCUM	ENTS	
Examiner Initials*	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Code ² known)	Kind (if	Cited Document	MM-DD-YYYY	
		6,020,252		Aspar et al	02/01/2000	
		5,985,681		Hamajima et al	11/16/1999	
		5,882,987		Srikrishnan	03/16/1999	
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		6,294,478	Bl	Sakaguchi et al	09/25/2001	
		6,271,101	B1	Fukunaga	08/07/2001	
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	FOREIGN PATENT DOCUMENTS									
Examiner Cite Initials No.1		Foreign Patent Document				Name of Patentee or	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or		
		Office ³	Number ⁴	Kind C (if kn		Applicant of Cited Document	MM-DD-YYYY	Relevant Figures Appear	T⁴	
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Examiner	Date	
Signature	Considered	

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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute	for form 1449A/PTO			Complete if Known			
INFORMATION DISCLOSURE				Application Number	Not Yet Assigned		
				Filing Date	August 4, 2002		
SIAI	STATEMENT BY APPLICANT			First Named Inventor	Takeshi FUKUNAGA		
(use as many sheets as necessary)				Group Art Unit	2823		
				Examiner Name	W. Brewster		
Sheet	2	of	2	Attorney Docket Number	740756-2641		

	OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS							
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.)., date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²					
		A.J. Auberton-Herve et al., "Industrial Research Society (Kogyo Chosa Kai); Electronic Material, pp. 83-87, August 1997 (Concise Statement attached)						
		"A Dissolved Wafer Process Using a Porous Sacrificial Layer and a Lightly-Doped Bulk Silicon Etch-Stop", Bell et al., Micro Electro Mechanical Systems, 1998, MEMS 98, Proceeding., The Eleventh Annual International Workshop on 1998, pp. 251-256.						
Examiner		Date						

Examiner		Date		
Signature		Considered	1	

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